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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1 of 2

Complete if Known	
Application Number	10/849,692
Filing Date	05/19/2004
First Named Inventor	Jared L. Zerbe
Art Unit	2661
Examiner Name	Unknown
Attorney Docket Number	RA328.P.US

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/DC/	1	US- 6,788,098 B1	09-07-2004	Alani et al.	
↓	2	US- 5,598,406	01-28-1997	Albrecht et al.	
	3	US- 2003/0117183 A1	06-26-2003	Thibeault et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3]-Number[4]-Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T[6]
/DC/	4	EP 1 424 871 A2	02.06.2004	Alcatel Canada Inc.		
↓	5	WO 00/65791	02.11.2000	Greigh, John L.		
	6	EP 0 886 407 A2	23.12.1998	Hewlett-Packard Company		

Examiner Signature	/Dady Chery/	Date Considered	08/09/2007

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/DC/	1	US- 6,150,863	11/21/2000	Conn et al.	
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FOREIGN PATENT DOCUMENTS

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Examiner Signature	/Dady Chery/	Date Considered	08/09/2007

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U.S. Department of Commerce, Patent and Trademark Office	Serial No.: Unknown
	Filing Date: May 19, 2004
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventor: Jared L. Zerbe
"Crosstalk Minimization in Serial Link Systems"	Group Art Unit: Unknown
	Examiner Name: Unknown
Express Mail No. ER 265640992 US	Attorney Docket No.: RA328.P.US

U.S. Patent and U.S. Patent Publication Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
/DC/	A	6,396,887 B1	05/28/02	Ware et al.	375	354	
	B	6,366,991 B1	04/02/02	Manning	711	167	
	C	5,509,038	04/16/96	Wicki	375	371	
	D	6,661,863 B1	12/09/03	Toosky	375	376	
	E	6,504,438 B1	01/07/03	Chang et al.	331	17	
	F	US 2003/ 0053489 A1	03/20/03	Zerbe et al.	370	503	
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U.S. Department of Commerce, Patent and Trademark Office	Serial No.: Unknown
	Filing Date: May 19, 2004
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Inventor: Jared L. Zerbe
"Crosstalk Minimization in Serial Link Systems"	Group Art Unit: Unknown
	Examiner Name: Unknown
Express Mail No. ER 265640992 US	Attorney Docket No.: RA328.P.US

U.S. Patent and U.S. Patent Publication Documents

*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate
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	X	ZERBE, JARED L. et al., "Equalization and Clock Recovery for a 2.5-10-Gb/s 2-PAM/4-PAM Backplane Transceiver Cell." IEEE Journal of Solid-State Circuits, Vol. 38, No. 12, December 2003. Pages 2121-2130.
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Examiner /Dady Chery/	Date Considered 08/09/2007

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